

## United States Patent and Trademark Office

COMMISSIONER FOR PATENTS
UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 2023I
www.uspto.gov



**CONFIRMATION NO. 5033** 

FILING DATE ATTORNEY **GROUP ART UNIT CLASS SERIAL NUMBER** DOCKET NO. 12/29/2000 <<del>2858</del>-09/751,750 2207/10083 **RULE** 2132 **APPLICANTS** Talal K. Jaber, Austin, TX; \*\* CONTINUING DATA \*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\* \*\* FOREIGN APPLICATIONS \*\*\*\*\*\*\*\*\*\*\*\* IF REQUIRED, FOREIGN FILING LICENSE GRANTED \*\* 02/14/2001 u yes ano Foreign Priority claimed STATE OR TOTAL INDEPENDENT SHEETS **CLAIMS** COUNTRY **DRAWING CLAIMS** Allowance 20 8 TX Verified and Examiner's Signature Acknowledged ADDRESS KENYON & KENYON Suite 600 333 W. San Carlos, Street CA 95110-2711, San Jose TITLE Microprocessor on-chip testing architecture and implementation All Fees 1.16 Fees ( Filing ) ☐ 1.17 Fees ( Processing Ext. of FILING FEE FEES: Authority has been given in Paper No. \_\_\_\_\_\_to charge/credit DEPOSIT ACCOUNT time) **RECEIVED** No. \_\_\_\_ for following: 1110 1.18 Fees ( Issue ) Other

☐ Credit